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| 18 | US 20040202269 A1 | 20041014 | 18 | Reception data synchronizing apparatus and method, and recording medium with recorded reception data synchronizing program | 375/368 |
| 19 | US 20040196937 A1 | 20041007 | 12 | Apparatus and method for clock adjustment in receiver of communication system | 375/355 |
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| 22 | US 20040101073 A1 | 20040527 | 27 | Method and apparatus for estimating response characteristic, and receiving method and receiver utilizing the same | 375/343 |
| 23 | US 20040028164 A1 | 20040212 | 21 | System and method for data transition control in a multirate communication system | 375/371 |
| 24 | US 20030185326 A1 | 20031002 | 25 | Frequency drift and phase error compensation in a VOFDM receiver | 375/371 |
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| 26 | US 20030152181 A1 | 20030814 | 26 | Delay locked loop synthesizer with multiple outputs and digital modulation | 375/371 |

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| 14 | Li, Henry |
| 15 | Yamaguchi, Hisakatsu |
| 16 | Yin, Guangming |
| 17 | Cranford, Hayden Clavie JR. et al. |
| 18 | Shimawaki, Kazuhiro |
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| 34 | US 20010046266 A1 | 20011129 | 149 | Apparatus and method for scdma digital data transmission using orthogonal codes and head end modem with no tracking loops | 375/259 |
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| 40 | US 7116742 B2 | 20061003 | 27 | Timing recovery system for a multi-pair gigabit transceiver | 375/355 |

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| 28 | Webster, Mark A. et al. |
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| 32 | Schmatz, Martin |
| 33 | Agazzi, Oscar E. et al. |
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| 35 | TRANS, FRANCOIS |
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| 43 | US 7092474 B2 | 20060815 | 27 | Linear phase detector for high-speed clock and data recovery | 375/375 |
| 44 | US 7092472 B2 | 20060815 | 53 | Data-level clock recovery | 375/371 |
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| 51 | US 7050522 B2 | 20060523 | 20 | Phase rotator and data recovery receiver incorporating said phase rotator | 375/371 |
| 52 | US 7035325 B2 | 20060425 | 10 | Jitter measurement using mixed down topology | 375/226 |
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| 41 | Morris; Martin |
| 42 | Chang; Kun-Yung K. et al. |
| 43 | Cao; Jun |
| 44 | Stojanovic; Vladimir M. |
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| 47 | MacIsaac; William Ross et al. |
| 48 | McBride; Brian E. |
| 49 | Long; Guozhu et al. |
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| 51 | Schmatz; Martin |
| 52 | Kleck; Jeffrey A. et al. |
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| 60 | US 6941484 B2 | 20050906 | 14 | Synthesis of a synchronization clock | 713/500 |
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| 65 | US 6850580 B1 | 20050201 | 20 | Bit synchronizing circuit | 375/355 |
| 66 | US 6804257 B1 | 20041012 | 12 | System and method for framing and protecting variable-length packet streams | 370/471 |
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| 70 | US 6768734 B2 | 20040727 | 10 | Device and method for equalizing data delays | 370/366 |
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| 60 | To; Hing Y. et al. |
| 61 | Doyle; James Thomas et al. |
| 62 | Desai; Chintan |
| 63 | Moser; James et al. |
| 64 | Betts; William Lewis et al. |
| 65 | Naoe; Hitoshi |
| 66 | Benayoun; Alain et al. |
| 67 | Riedle; Christopher G. et al. |
| 68 | Young; Steven P. et al. |
| 69 | Agazzi; Oscar E. et al. |
| 70 | Wenk; Christian |
| 71 | Dunning; David S. et al. |
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| 79 | US 6690757 B1 | 20040210 | 16 | High-speed interconnection adapter having automated lane de-skew | 375/371 |
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| 84 | US 6636573 B2 | 20031021 | 38 | Precision timing generator system and method | 375/355 |
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| 76 | Long; Guozhu et al. |
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| 78 | Stine; Eric |
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| 93 | Buchanan; Brian et al. |
| 94 | O'Shea; Deirdre et al. |
| 95 | Beukema; Troy J. |
| 96 | Mohindra; Rishi |
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| 113 | US 6115075 A | 20000905 | 67 | Method and apparatus for adjusting dot clock signal | 348/537 |
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| 123 | US 6014417 A | 20000111 | 12 | On-chip phase step generator for a digital phase locked loop | 375/374 |
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| 125 | US 6002731 A | 19991214 | 18 | Received-data bit synchronization circuit | 375/371 |
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| 111 | Hussein; Ershad et al. |
| 112 | de Lantremange; Maximilien d'Oreye |
| 113 | Yoneno; Kunio |
| 114 | Yonge, III; Lawrence W. |
| 115 | Nobusawa; Hideaki |
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| 120 | Oura; Hideto et al. |
| 121 | Lee; Kathleen Otis et al. |
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| 130 | US 5933454 A | 19990803 | 17 | Multi-carrier data transmissions system using an overhead bus for synchronizing multiple remote units | 375/260 |
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| 134 | US 5898665 A | 19990427 | 35 | Coherent tracking apparatus and method for CDMA receiver | 370/342 |
| 135 | US 5850422 A | 19981215 | 14 | Apparatus and method for recovering a clock signal which is embedded in an incoming data stream | 375/371 |
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| 140 | US 5757857 A | 19980526 | 22 | High speed self-adjusting clock recovery circuit with frequency detection | 375/271 |
| 141 | US 5751775 A | 19980512 | 16 | Transmission circuit of a line encoded signal on a telephone line | 375/371 |

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| 128 | Zhodzicshsky; Mark et al. |
| 129 | Wong; Hee et al. |
| 130 | Cioffi; John M. |
| 131 | Wilhite; Jeffrey B. et al. |
| 132 | Fujimura; Akinori |
| 133 | Watanabe; Hidekazu et al. |
| 134 | Sawahashi; Mamoru et al. |
| 135 | Chen; Dao-Long |
| 136 | Ferraiolo; Frank David et al. |
| 137 | Hurlbut; Amy O. et al. |
| 138 | Lennen; Gary R. |
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| 149 | US 5568526 A | 19961022 | 10 | Self timed interface | 375/356 |
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| 151 | US 5550876 A | 19960827 | 9 | Measuring device for a synchronous transmission system | 375/371 |
| 152 | US 5535252 A | 19960709 | 20 | Clock synchronization circuit and clock synchronizing method in baseband demodulator of digital modulation type | 375/371 |
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| 154 | US 5533072 A | 19960702 | 22 | Digital phase alignment and integrated multichannel transceiver employing same | 375/371 |
| 155 | US 5511091 A | 19960423 | 13 | Clock synchronization control check system | 375/226 |
| 156 | US 5491729 A | 19960213 | 20 | Digital phase-locked data recovery circuit | 375/376 |
| 157 | US 5485490 A | 19960116 | 25 | Method and circuitry for clock synchronization | 375/371 |
| 158 | US 5475715 A | 19951212 | 18 | Sync data introduction method and system | 375/354 |
| 159 | US 5459753 A | 19951017 | 17 | Method and apparatus for pattern independent phase detection and timing recovery | 375/362 |

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| 143 | Beladi; S. Hossein et al. |
| 144 | Berger; Lior |
| 145 | Read; Andrew J. et al. |
| 146 | Roither; Gerhard |
| 147 | Betts; Robert et al. |
| 148 | Jeong; Deog-Kyo |
| 149 | Ferraiolo; Frank D. et al. |
| 150 | Kushige; Naohide et al. |
| 151 | Urbansky; Ralph |
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